Moser Patterson & Sheridan, llp

- ATTORNEYS AT LAW

FEB 2 5 2005

3040 Post Oak Blvd, Suite 1500 Houston, TX 77056-6582 TEL 713.623.4844 FAX 713.623.4846

WWW.MPSLLP.COM

## FACSIMILE COVER SHEET

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DATE:

February 25, 2005

**FILE NO:** 

INFN/WB0037

TO:

MAIL STOP AMENDMENT

Examiner Calvin Lee

**FAX NO:** 

1-703-872-9306

FROM:

Gero G. McClellan

PAGE(S) with cover:

11

**ORIGINAL TO** 

**FOLLOW?** 

☐ YES ⊠ NO

RE:

TITLE:

DRAM CELL ARRANGEMENT WITH VERTICAL MOS

TRANSISTORS, AND METHOD FOR ITS FABRICATION

U.S. SERIAL NO.:

10/720,730

FILING DATE:

November 24, 2003

INVENTOR(\$):

Till Schlösser et al.

**EXAMINER:** 

Calvin Lee

GROUP ART UNIT:

2825

CONFIRMATION NO.:

2757

Attached for the above-referenced application are:

- 1. TRANSMITTAL
- 2. RESPONSE TO RESTRICTION REQUIREMENT DATED JANUARY 25, 2005.
- 3. SUPPLEMENTAL DECLARATION

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TRANSMITTAL FORM		Application Number		10/720,730		
		Filing Date		November 24, 2003		
		First Named Inventor		Till Schlösser		
	Art Unit		2812			
(to be used for all correspondence after	Examiner Name		Calvin Le	<del></del>		
Total Number of Pages in This Submission 10		Attorney Docket N	lumber	INFN/WB	0037	
ENCLOSURES (check all that apply)						
Fee Transmittal Form	Drawing(s			After All	owance Communication to TC	
Fee Attached	Licensing-related Papers			Appeal Communication to Board of Appeals and Interferences		
Amendment / Reply	Petition		Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)			
After Final	Petition to Convert to a Provisional Application			Proprietary Information		
Affidavits/declaration(s)	Power of Attorney, Revocation Change of Correspondence Address			Status Letter		
Extension of Time Request	Terminal Disclaimer			Other Enclosure(s) (please identify below):		
Express Abandonment Request	Request for Refund		Supplement	al Declaration		
CD, Num		er of CD(s)				
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Firm	MOSER, PATTERSON & SHERIDAN, LLP					
Signature	f-//_					
Printed Name	Gero G. Mcclelian					
Date	February 25	, 2005	Reg. No.	44,227		
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FEB 2 5 2005

**PATENT** 

W&B Ref. No.: INF 2008-US/PC Atty. Dkt. No. INFN/WB0037

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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In re Application of:

Till Schlösser et al.

Serial No.:

10/720,730

Confirmation No.: 2757

Filed:

November 24, 2003

For:

Feb-25-05

DRAM CELL ARRANGEMENT

WITH VERTICAL MOS

TRANSISTORS, AND METHOD

FOR ITS FABRICATION

MAIL STOP AMENDMENT Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

Group Art Unit:

2825

Examiner:

Calvin Lee

CERTIFICATE OF FAX 37 CFR 1.8

I hereby certify that this correspondence is being facsifulle transmitted to the Patent and Trademary Office to tax purpher 703/872-9306 to the attention of Examiner Calvin date below:

February 25, 2005

Gero G. McClellan

## **RESPONSE TO RESTRICTION REQUIREMENT DATED JANUARY 25, 2005**

In response to the Restriction Requirement dated January 25, 2005, having a shortened statutory period for response set to expire on February 25, 2005, please enter this response and reconsider the claims pending in the application for reasons discussed below. While no fees are believed due, the Commissioner is hereby authorized to charge counsel's Deposit Account No. 20-0782/INFN/WB0037/GGM for any fees, including extension of time fees or excess claim fees, required to make this response timely and acceptable to the Office.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper. Remarks/Arguments begin on page 6 of this paper.

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